SEMICONDUCTOR MANUFACTURING II

eLearning courses designed to increase productivity and profits



Learning Made Simple, Visual, and Interactive

Semiconductor manufacturing is a complex process involving many steps. The THORS Semiconductor Manufacturing II course focuses on the semiconductor patterning process. Information on photoresist application, use of reticles, different lithography techniques, and advanced methods of patterning are covered in this course. To enhance the learning experience, the course contains interactive guizzes that allow learners to test their knowledge and retention of the visually engaging content.

Credit Hours 1.5

Learning Objectives

- Describe the various steps involved in the pre-patterning process. Ő
- Identify the various types of lithography techniques and their applications.
- Recognize the various types of pattern printing techniques employed in the semiconductor industry.
- × (j) Explain the importance of post-exposure processes.
- ΞŒ)Ξ Define the different advanced methods of patterning and their advantages.

Table of Contents

I. Pre-Patterning Processes

- Photoresist
 - Photoresist Chemistry
 - Equipment
 - Photoresist Baking
- Reticle

II. Photolithography

- Lithography Techniques
 - Optical Lithography
 - Extreme Ultraviolet (EUV) Lithography
 - Deep Ultraviolet (DUV) Lithography
 - Immersion Lithography
 - Emerging Lithography
 - Nanoimprint Lithography
 - Other Emerging Lithography
- Pattern Printing Techniques Shadow Printing

II. Photolithography (continued)

- Projection Printing
 - Stepping
 - Scanning
- Step and Scan
- Post-Exposure Processes
- Post-Exposure Bake (PEB) Development
- Advanced Methods of Patterning
- Double Patterning
 - Litho Etch Litho Etch (LELE)
 - Self-Aligned Double Patterning (SADP)
- Triple Patterning





THORS is bringing together the best minds across many industries to create an everexpanding library of courses that will rapidly increase the Manufacturing IQ® of your team. sales@thors.com 1 (330) 576 4448

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